CALL FOR PAPERS IEEE INTERNATIONAL MWSCAS 2017

The IEEE International Midwest Symposium on Circuits and Systems is the oldest Circuits and Systems Symposium sponsored by IEEE. The 60th edition will be held on the campus of Tufts University, Boston, MA, USA, August 6 - 9, 2017. MWSCAS 2017 will include oral and poster sessions, student paper contest, tutorials given by experts in circuits and systems topics, and special sessions. Topics include, but are not limited to:

**Track 1. Analog Circuits and Systems I**
- 1.1 Analog Circuits
- 1.2 Analog Systems
- 1.3 Biomedical Electronics
- 1.4 Bioengineering Systems and Bio Chips
- 1.5 Other Analog Circuits and Systems

**Track 2. Analog Circuits and Systems II**
- 2.1 Linear Analog Systems
- 2.2 Non-Linear Analog Systems
- 2.3 System Architectures
- 2.4 Neuromorphic Systems

**Track 3. Digital Circuits and Systems I**
- 3.1 Digital Integrated Circuits
- 3.2 System On a Chip (SOC) and Network on a Chip (NOC)

**Track 4. Digital Circuits and Systems II**
- 4.1 Digital Filters
- 4.2 Hardware-Software Co-Design
- 4.3 Other Digital Circuits and Systems

**Track 5. Communications Circuits and Systems**
- 5.1 Communications Circuits, Computers and Applications
- 5.2 Communications Systems and Control
- 5.3 Information Theory, Coding and Security
- 5.4 Communications Theory
- 5.5 Other Communications Circuits and Systems

**Track 6. RF and Wireless Circuits and Systems**
- 6.1 RF Front-End Circuits
- 6.2 Mixed-Signal RF and Analog and Baseline Circuits
- 6.3 Wireless Mobile Circuits and Systems and Connectivity
- 6.4 VCO’s and Frequency Multipliers, PLL’s and Synthesizers
- 6.5 Other RR and Wireless Circuits and Systems

**Track 7. Sensor Circuits and Systems**
- 7.1 Technologies for Smart Sensors
- 7.2 Sensor Fusion
- 7.3 Control Systems
- 7.4 Mechatronics and Robotics
- 7.5 Other Sensor Circuits and Systems

**Track 8. Converter Circuits and Systems**
- 8.1 Analog to Digital Converters
- 8.2 Digital to Analog Converters
- 8.3 DC-DC Converters
- 8.4 Other Converter Circuits and Systems

**Track 9. Signal and Image Processing**
- 9.1 Analog and Mixed Signal Processing
- 9.2 Digital Signal Processing
- 9.3 Signal Processing Theory and Methods
- 9.4 Image, Video and Multi-Dimensional Signal Processing
- 9.5 Other Signal and Image Processing

**Track 10. Hardware Design**
- 10.1 Processor and Memory Design
- 10.2 MEMS/NEMS
- 10.3 Nano-Electronics and Technology
- 10.4 Optics and Photonics
- 10.5 Power Management, Power Harvesting and Power Electronics
- 10.6 Photovoltaic Devices/Panels and Energy Harvesting

**Track 11. Hardware Security**
- 11.1 Hardware Authentication and Physically Unclonable Functions (PUFs)
- 11.2 Trusted Microelectronics
- 11.3 Hardware Anti-Tamper
- 11.4 Architectural System Security
- 11.5 Other Hardware Security

Prospective authors are invited to submit a full paper (4 pages) describing original work. Only electronic submissions will be accepted. Papers should include title, abstract, and topic category from the list above in standard IEEE two-column format for consideration as lecture or poster. Both formats have the same value, and presentation method will be chosen for suitability. All submissions should be made electronically through the MWSCAS 2017 web site (http://www.mwscas2017.org). Students are encouraged to participate in the best student paper award contest. Accepted papers will be published in the conference proceedings subject to advance registration of at least one of the authors.

**IMPORTANT DATES**
- March 18: Tutorial and Special Session proposals deadline
- March 18: Regular and Student paper submission deadline
- April 1: Special session and invited paper submission deadline
- April 29: Notice of acceptance
- May 20: Final camera-ready paper deadline

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